

RICHARD WILSON VUDUC

CURRICULUM VITÆ

Georgia Institute of Technology  
College of Computing  
Computational Science and Engineering Division

266 Ferst Drive  
Atlanta, Georgia 30332-0765, USA  
+1 404.385.3355 (voice)  
+1 404.385.7337 (fax)  
[richie@cc.gatech.edu](mailto:richie@cc.gatech.edu)  
[www.cc.gatech.edu/~richie](http://www.cc.gatech.edu/~richie)

Richard Wilson Vuduc: *Curriculum Vitæ*, © August 2010

[ August 19, 2010 at 1:03 ]

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Part I  
CURRICULUM VITÆ

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 BIOGRAPHICAL DATA
 

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## 1.1 EDUCATIONAL BACKGROUND

DEGREE	YEAR	UNIVERSITY	FIELD
Ph.D	2004	University of California, Berkeley	Computer Science
B.S.	1997	Cornell University	Computer Science

## 1.2 EMPLOYMENT HISTORY

TITLE	ORGANIZATION	WHEN
Assistant Professor	Georgia Tech	Aug. '07–present
Postdoc	Lawrence Livermore National Laboratory	Nov. '04–Jul. '07
Postdoc	University of California, Berkeley	Jan. '04–Oct. '04
Postdoc	University of California, Berkeley	Aug. '97–Jan. '04
Research Intern	Institute for Defense Analyses	Aug. '94–Jun. '96

## 1.3 CURRENT FIELDS OF INTEREST

MY RESEARCH IN HIGH-PERFORMANCE COMPUTING (HPC) SYSTEMS addresses fundamental questions of how to analyze, to tune, and to debug software automatically for complex and rapidly-evolving architectures, including current multicore and future manycore systems. I am particularly interested in these questions from the perspective of computational science and engineering (CSE) applications, including those that require massive-scale data analysis.

The following are my current focus areas:

- High-performance parallel programming models
- Automated performance tuning, or “autotuning”
- Performance analysis
- Performance and correctness debugging

I believe that the advances my research lab is making in these areas, when taken together, are helping to create more productive environments for developing high-performance code.

## TEACHING

## 2.1 COURSES TAUGHT

WHEN	COURSE	SIZE	SCORE	NOTES
Spring '10	CS 4225: Introduction to High-Performance Computing	17	4.5	
Fall '09	CSE 6230: High-performance Computing: Tools and Applications	13	4.9	
Spring '09	CS 4643 / MATH 4641: Numerical Analysis II	—	—	Cancelled
Fall '08	CSE / CS 6230: High-Performance Computing: Tools and Applications	35	4.1	New version
Spring '08	CSE 8803 PNA: Parallel Numerical Algorithms	21	4.6	New course

NOTE: Scores indicate the *Overall Teaching Effectiveness* summary rating (maximum value is 5.0) given by students in the Georgia Tech Course-Instructor Opinion Survey (CIOS).

## 2.2 INDIVIDUAL STUDENT GUIDANCE

2.2.1 *Ph.D. Students Supervised (primary)*

- Cong Hou (CoC/CS)  
Summer 2009–present
- Jee CHOI (CoE/ECE)  
Fall 2008–present
- Aparna CHANDRAMOWLISHWARAN (CoC/CSE)  
Fall 2008–present

2.2.2 *Ph.D. Students Supervised (as co-advisor)*

- George VULOV (CoC/CSE)  
Fall 2009–present
- Sangmin PARK (CoC/CS)  
Fall 2008–present

2.2.3 *M.S. Thesis Students Supervised*

- Sundaresan VENKATASUBRAMANIAN (CoC/CS)  
Spring 2008  
THESIS: Tuned and wildly asynchronous stencil kernels for hybrid CPU/GPU platforms.  
COMMITTEE: Richard VUDUC (advisor, CoC/CSE), Hyesoon KIM (CoC/CS), Jeffrey VETTER (CoC/CSE & ORNL)  
PLACED: Amazon.com

2.2.4 *Undergraduate Students Supervised*

Students are from Georgia Tech unless otherwise noted.

- New '10-'11 Item* • Japnik SINGH (IIT-Bombay)  
Summer 2010
- New '10-'11 Item* • Piyush Kumar SAO (IIT-Madras)  
Summer 2010
- New '10-'11 Item* • Ashish NARASIMHAM  
Fall 2009–Spring 2010
- Nimit NIGANIA (IIT-Madras)  
Summer 2009
- Amik SINGH (IIT-Roorkee)  
Summer 2009
- Jada JEFFRIES (Hampton University)  
Summer 2009
- Gaurav CHADHA (IIT-Guwahati)  
Summer 2008  
PLACED: Ph.D. program at U. Michigan, EECS Dept.
- Lauren GRACIA (U. Houston–Downtown)  
Summer 2008  
PLACED: Chevron

## 2.3 TEACHING HONORS AND AWARDS

- New '10-'11 Item* • THANK-A-TEACHER  
Spring 2010  
CETL and GT Student Ambassadors
- THANK-A-TEACHER  
Spring 2009  
CETL and GT Student Ambassadors

- OUTSTANDING GRADUATE STUDENT INSTRUCTOR (UC Berkeley)  
Fall 1997  
Awarded to fewer than 10% of graduate students campus-wide.



## RESEARCH

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### 3.1 THESES

- **PH.D.:** [Automatic performance tuning of sparse matrix kernels](#)  
COMPLETED: January 2004  
ADVISORS: James W. DEMMEL (primary), Katherine A. YELICK

### 3.2 JOURNAL ARTICLES (REFEREED)

- [J1] Sam Williams, Richard Vuduc, Leonid Oliker, John Shalf, Katherine Yelick, and James Demmel. Optimizing sparse matrix-vector multiply on emerging multicore platforms. *Parallel Computing (ParCo)*, 35(3):178–194, March 2009.  
*Extends conference version:* <http://dx.doi.org/10.1145/1362622.1362674>.  
**Most Downloaded Paper, Q1 2009:** <http://tinyurl.com/yllkfee>.
- [J2] Rajesh Nishtala, Richard Vuduc, James W. Demmel, and Katherine A. Yelick. When cache blocking sparse matrix vector multiply works and why. *Applicable Algebra in Engineering, Communication, and Computing: Special Issue on Computational Linear Algebra and Sparse Matrix Computations*, March 2007.
- [J3] James Demmel, Jack Dongarra, Viktor Eijkhout, Erika Fuentes, Antoine Petit, Richard Vuduc, R. Clint Whaley, and Katherine Yelick. Self-adapting linear algebra algorithms and software. *Proc. IEEE*, 93(2):293–312, February 2005.
- [J4] Eun-Jin Im, Katherine Yelick, and Richard Vuduc. SPARSITY: Optimization framework for sparse matrix kernels. *Int'l. J. High Performance Computing Applications (IJHPCA)*, 18(1):135–158, February 2004.
- [J5] Richard Vuduc, James Demmel, and Jeff Bilmes. Statistical models for empirical search-based performance tuning. *Int'l. J. High Performance Computing Applications (IJHPCA)*, 18(1):65–94, 2004.  
*Extends conference version:* [http://dx.doi.org/10.1007/3-540-45545-0\\_21](http://dx.doi.org/10.1007/3-540-45545-0_21).
- [J6] Bohdan Balko, Irvin W. Kay, James D. Silk, Richard Vuduc, and John W. Neuberger. Superfluorescence in the presence of inhomogeneous broadening. *Hyperfine Interactions: Special Issue on the Gamma-Ray Laser*, 107(1–4):369–379, June 1997.
- [J7] Bohdan Balko, Irvin W. Kay, Richard Vuduc, and John W. Neuberger. Recovery of superfluorescence in inhomogeneously broad-

ened systems through rapid relaxation. *Phys. Rev. B*, 55(18):12079–12085, May 1997.

### 3.3 BOOK CONTRIBUTIONS

- New '10-'11 Item* [B1] Richard W. Vuduc. Autotuning. In David Padua, editor, *Encyclopedia of Parallel Computing*. Springer, 2010.
- New '10-'11 Item* [B2] Sam Williams, Nathan Bell, Jee Choi, Michael Garland, Leonid Oliker, and Richard Vuduc. Sparse matrix vector multiplication on multicore and accelerator systems. In Jack Dongarra, David A. Bader, and Jakub Kurzak, editors, *Scientific Computing with Multi-core Processors and Accelerators*. CRC Press, 2010.

### 3.4 CONFERENCE PUBLICATIONS (REFEREED)

- New '10-'11 Item* [C1] Jaekyu Lee, Nagesh B. Lakshminarayana, Hyesoon Kim, and Richard Vuduc. Hardware and software prefetching mechanisms for GPGPU applications. In *Proc. IEEE/ACM Int'l. Symp. Microarchitecture (MICRO)*, Atlanta, GA, USA, December 2010. (accepted).  
Acceptance rate:  $[45/248=18.1\%]$ .
- New '10-'11 Item* [C2] Aparna Chandramowlishwaran, Kamesh Madduri, and Richard Vuduc. Diagnosis, tuning, and redesign for multicore performance: A case study of the fast multipole method. In *Proc. ACM/IEEE Conf. Supercomputing (SC)*, New Orleans, LA, USA, November 2010. (to appear).
- New '10-'11 Item* [C3] Abtin Rahimian, Ilya Lashuk, Aparna Chandramowlishwaran, Dhairya Malhotra, Logan Moon, Rahul Sampath, Aashay Shringarpure, Shravan Veerapaneni, Jeffrey Vetter, Richard Vuduc, Denis Zorin, and George Biros. Petascale direct numerical simulation of blood flow on 200k cores and heterogeneous architectures. In *Proc. ACM/IEEE Conf. Supercomputing (SC)*, New Orleans, LA, USA, November 2010. (to appear).  
**Finalist, Gordon Bell Prize.**
- New '10-'11 Item* [C4] Sangmin Park, Richard W. Vuduc, and Mary Jean Harrold. FALCON: Fault localization for concurrent programs. In *Proc. ACM/IEEE Int'l. Conf. Software Eng.*, Cape Town, South Africa, May 2010.  
Acceptance rate:  $[52/380=13.7\%]$ .
- New '10-'11 Item* [C5] Aparna Chandramowlishwaran, Kathleen Knobe, and Richard Vuduc. Performance evaluation of Concurrent Collections on high-performance multicore computing systems. In *Proc. IEEE Int'l. Parallel and Distributed Processing Symp. (IPDPS)*, Atlanta, GA, USA, April 2010.  
**Winner, Best Paper (software track).**  
Acceptance rate:  $[127/527=24.1\%]$ .

- [C6] Aparna Chandramowliswaran, Samuel Williams, Leonid Oliker, Ilya Lashuk, George Biros, and Richard Vuduc. Optimizing and tuning the fast multipole method for state-of-the-art multicore architectures. In *Proc. IEEE Int'l. Parallel and Distributed Processing Symp. (IPDPS)*, Atlanta, GA, USA, April 2010. Acceptance rate: [127/527=24.1%]. New '10-'11 Item
- [C7] Jee Whan Choi, Amik Singh, and Richard W. Vuduc. Model-driven autotuning of sparse matrix-vector multiply on GPUs. In *Proc. ACM SIGPLAN Symp. Principles and Practice of Parallel Programming (PPoPP)*, Bangalore, India, January 2010. Acceptance rate: [29/173=16.8%]. New '10-'11 Item  
<http://dx.doi.org/10.1145/1693453.1693471>.
- [C8] Aparna Chandramowliswaran, Kathleen Knobe, and Richard Vuduc. Applying the Concurrent Collections programming model to asynchronous parallel dense linear algebra. In *Proc. ACM SIGPLAN Symp. Principles and Practice of Parallel Programming (PPoPP)*, Bangalore, India, January 2010. (poster). New '10-'11 Item  
Acceptance rate: [Papers+posters: 45/173=26.1%].  
<http://dx.doi.org/10.1145/1693453.1693506>.
- [C9] Ilya Lashuk, Aparna Chandramowliswaran, Harper Langston, Tuan-Anh Nguyen, Rahul Sampath, Aashay Shringarpure, Richard Vuduc, Lexing Ying, Denis Zorin, and George Biros. A massively parallel adaptive fast multipole method on heterogeneous architectures. In *Proc. ACM/IEEE Conf. Supercomputing (SC)*, Portland, OR, USA, November 2009.  
**Finalist, Best Paper.**  
Acceptance rate: [59/261=22.6%].  
<http://doi.acm.org/10.1145/1654059.1654118>.
- [C10] Nitin Arora, Aashay Shringarpure, and Richard Vuduc. Direct  $n$ -body kernels for multicore platforms. In *Proc. Int'l. Conf. Parallel Processing (ICPP)*, Vienna, Austria, September 2009. Acceptance rate: [71/220=32.3%].  
<http://dx.doi.org/10.1109/ICPP.2009.71>.
- [C11] Nitin Arora, Ryan P. Russell, and Richard W. Vuduc. Fast sensitivity computations for numerical optimizations. In *Proc. AAS/AIAA Astrodynamics Specialist Conference*, AAS 09-435, Pittsburgh, PA, USA, August 2009.  
[http://soliton.ae.gatech.edu/people/russell/FinalPublications/ConferencePapers/09AugAAS\\_09-392\\_2pLowthrust.pdf](http://soliton.ae.gatech.edu/people/russell/FinalPublications/ConferencePapers/09AugAAS_09-392_2pLowthrust.pdf).
- [C12] Sundaresan Venkatasubramanian and Richard W. Vuduc. Tuned and wildly asynchronous stencil kernels for hybrid CPU/GPU platforms. In *Proc. ACM Int'l. Conf. Supercomputing (ICS)*, New York, NY, USA, June 2009. Acceptance rate: [47/191=25%].  
<http://dx.doi.org/10.1145/1542275.1542312>.

- [C13] Seunghwa Kang, David Bader, and Richard Vuduc. Understanding the design trade-offs among current multicore systems for numerical computations. In *Proc. IEEE Int'l. Parallel and Distributed Processing Symp. (IPDPS)*, Rome, Italy, May 2009.  
Acceptance rate: [101/440=23.0%].  
<http://doi.ieeecomputersociety.org/10.1109/IPDPS.2009.5161055>.
- [C14] Manisha Gajbe, Andrew Canning, John Shalf, Lin-Wang Wang, Harvey Wasserman, and Richard Vuduc. Optimization and auto-tuning of 3D FFTs on the Cray XT4. In *Proc. Cray User's Group (CUG) Meeting*, Atlanta, GA, USA, May 2009.
- [C15] Sam Williams, Leonid Oliker, Richard Vuduc, John Shalf, Katherine Yelick, and James Demmel. Optimization of sparse matrix-vector multiplication on emerging multicore platforms. In *Proc. ACM/IEEE Conf. Supercomputing (SC)*, 2007.  
Acceptance rate: [54/268=20.1%].  
<http://dx.doi.org/10.1145/1362622.1362674>.
- [C16] Dan Quinlan, Richard Vuduc, Thomas Panas, Jochen Härdtlein, and Andreas Sæbjørnsen. Support for whole-program analysis and the verification of the one-definition rule in C++. In *Proc. Static Analysis Summit (SAS)*, volume NIST Special Publication 500-262, pages 27–35, 2006.  
[http://samate.nist.gov/docs/NIST\\_Special\\_Publication\\_500-262.pdf](http://samate.nist.gov/docs/NIST_Special_Publication_500-262.pdf).
- [C17] Dan Quinlan, Shmuel Ur, and Richard Vuduc. An extensible open-source compiler infrastructure for testing. In *Proc. IBM Haifa Verification Conf. (VC)*, volume LNCS 3875, pages 116–133, Haifa, Israel, November 2005. Springer Berlin / Heidelberg.  
[http://dx.doi.org/10.1007/11678779\\_9](http://dx.doi.org/10.1007/11678779_9).
- [C18] Richard W. Vuduc and Hyun-Jin Moon. Fast sparse matrix-vector multiplication by exploiting variable block structure. In *Proc. High-Performance Computing and Communications Conf. (HPCC)*, volume LNCS 3726, pages 807–816, Sorrento, Italy, September 2005. Springer.  
Acceptance rate: [116/387=30%].  
[http://dx.doi.org/10.1007/11557654\\_91](http://dx.doi.org/10.1007/11557654_91).
- [C19] Richard Vuduc, James W. Demmel, and Katherine A. Yelick. OSKI: A library of automatically tuned sparse matrix kernels. In *Proc. SciDAC, J. Physics: Conf. Ser.*, volume 16, pages 521–530, 2005.  
<http://dx.doi.org/10.1088/1742-6596/16/1/071>.
- [C20] Benjamin C. Lee, Richard Vuduc, James Demmel, and Katherine Yelick. Performance models for evaluation and automatic tuning of symmetric sparse matrix-vector multiply. In *Proc. Int'l. Conf. Parallel Processing (ICPP)*, Montreal, Canada, August 2004.  
**Winner, Best Paper.**  
Acceptance rate: [65/190=34.2%].  
<http://dx.doi.org/10.1109/ICPP.2004.1327917>.

- [C21] Richard Vuduc, James W. Demmel, Katherine A. Yelick, Shoaib Kamil, Rajesh Nishtala, and Benjamin Lee. Performance optimizations and bounds for sparse matrix-vector multiply. In *Proc. ACM/IEEE Conf. Supercomputing (SC)*, Baltimore, MD, USA, November 2002.

**Finalist, Best Student Paper.**

<http://portal.acm.org/citation.cfm?id=762822>.

- [C22] Richard Vuduc, James W. Demmel, and Jeff A. Bilmes. Statistical models for empirical search-based performance tuning. In *Proc. Int'l. Conf. Computational Science (ICCS)*, volume LNCS 2073, pages 117–126, San Francisco, CA, USA, May 2001. Springer Berlin / Heidelberg.

*Extends workshop version:* <http://www.eecs.harvard.edu/~smith/fddo3/papers/107.ps>.

[http://dx.doi.org/10.1007/3-540-45545-0\\_21](http://dx.doi.org/10.1007/3-540-45545-0_21).

- [C23] Danyel Fisher, Kris Hildrum, Jason Hong, Mark Newman, Megan Thomas, and Richard Vuduc. SWAMI: A framework for collaborative filtering algorithm development and evaluation. In *Proc. ACM Conf. Research and Development in Information Retrieval (SIGIR)*, pages 366–368, Athens, Greece, July 2000. (poster).

<http://dx.doi.org/10.1145/345508.345658>.

- [C24] Bohdan Balko, Irvin Kay, Richard Vuduc, and John Neuberger. An investigation of the possible enhancement of nuclear superfluorescence. In *Proc. Lasers '95*, page 308, 1996.

### 3.5 WORKSHOP PUBLICATIONS (REFEREED)

- [W1] Richard Vuduc, Aparna Chandramowlishwaran, Jee Whan Choi, Murat Efe Guney, and Aashay Shringarpure. On the limits of GPU acceleration. In *Proc. USENIX Wkshp. Hot Topics in Parallelism (HotPar)*, Berkeley, CA, USA, June 2010.

*New '10-'11 Item*

Acceptance rate: [Talks: 16/68=23.5%].

- [W2] Sooraj Bhat, Ashish Agarwal, Alexander Gray, and Richard Vuduc. Toward interactive statistical modeling. *Procedia Computer Science*, 1(1):1829–1838, May–June 2010.

*New '10-'11 Item*

*Proc. Int'l. Conf. Computational Science (ICCS), Wkshp. Automated Program Generation for Computational Science (APGCS).*

Acceptance rate: [10/21=47.6%].

- [W3] Chunhua Liao, Daniel J. Quinlan, Richard Vuduc, and Thomas Panas. Effective source-to-source outlining to support whole program empirical optimization. In *Proc. Int'l. Wkshp. Languages and Compilers for Parallel Computing (LCPC)*, volume LNCS, Newark, DE, USA, October 2009.

[http://www.osti.gov/bridge/product.biblio.jsp?osti\\_id=966918](http://www.osti.gov/bridge/product.biblio.jsp?osti_id=966918).

- [W4] Aparna Chandramowlishwaran, Abhinav Karhu, Ketan Umare, and Richard Vuduc. Numerical algorithms with tunable

parallelism. In *Proc. Wkshp. Software Tools for Multicore Systems (STMCS)*, at *IEEE/ACM Int'l. Symp. Code Generation and Optimization (CGO)*, Boston, MA, USA, April 2008.

<http://people.csail.mit.edu/rabbah/conferences/08/cgo/stmcs/papers/vuduc-stmcs08.pdf>.

- [W5] Thomas Panas, Dan Quinlan, and Richard Vuduc. Analyzing and visualizing whole program architectures. In *Proc. Wkshp. Aerospace Software Engineering (AeroSE)*, at *ACM/IEEE Int'l. Conf. Software Eng. (ICSE)*, Minneapolis, MN, USA, May 2007. Also: Lawrence Livermore National Laboratory Technical Report UCRL-PROC-231453.

<http://www.osti.gov/bridge/servlets/purl/909924-c8K5TR/909924.pdf>.

- [W6] Thomas Panas, Dan Quinlan, and Richard Vuduc. Tool support for inspecting the code quality of HPC applications. In *Proc. Wkshp. Software Eng. for High-Performance Computing Applications (SE-HPC)*, at *ACM/IEEE Int'l. Conf. Software Eng. (ICSE)*, Minneapolis, MN, USA, May 2007.

<http://dx.doi.org/10.1109/SE-HPC.2007.8>.

- [W7] Dan Quinlan, Richard Vuduc, and Ghassan Misherghi. Techniques for specifying bug patterns. In *Proc. ACM Wkshp. Parallel and Distributed Systems: Testing and Debugging (PADTAD)*, at *Int'l. Symp. Software Testing and Analysis (ISSTA)*, Portland, ME, USA, July 2007.

**Winner, Best Paper.**

<http://doi.acm.org/10.1145/1273647.1273654>.

- [W8] Qing Yi, Keith Seymour, Haihang You, Richard Vuduc, and Dan Quinlan. POET: Parameterized Optimizations for Empirical Tuning. In *Proc. Wkshp. Performance Optimization of High-level Languages and Libraries (POHLL)*, at *IEEE Int'l. Par. Distrib. Processing Symp. (IPDPS)*, pages 1–8, Long Beach, CA, USA, March 2007.

<http://dx.doi.org/10.1109/IPDPS.2007.370637>.

- [W9] Dan Quinlan, Markus Schordan, Richard Vuduc, and Qing Yi. Annotating user-defined abstractions for optimization. In *Proc. Wkshp. Performance Optimization of High-level Languages and Libraries (POHLL)*, at *IEEE Int'l. Par. Distrib. Processing Symp. (IPDPS)*, Rhodes, Greece, April 2006.

<http://dx.doi.org/10.1109/IPDPS.2006.1639722>.

- [W10] Richard Vuduc, Martin Schulz, Dan Quinlan, and Bronis de Supinski. Improving distributed memory applications testing by message perturbation. In *Proc. ACM Wkshp. Parallel and Distributed Systems: Testing and Debugging (PADTAD)*, at *Int'l. Symp. Software Testing and Analysis (ISSTA)*, Portland, ME, USA, July 2006.

**Winner, Best Paper.**

- [W11] Richard Vuduc, Attila Gyulassy, James W. Demmel, and Katherine A. Yelick. Memory hierarchy optimizations and bounds for sparse  $A^T Ax$ . In *Proc. Wkshp. Parallel Linear Algebra (PLA), at Int'l. Conf. Computational Sci. (ICCS)*, volume LNCS 2659, pages 705–714, Melbourne, Australia, June 2003. Springer Berlin / Heidelberg.  
[http://dx.doi.org/10.1007/3-540-44863-2\\_69](http://dx.doi.org/10.1007/3-540-44863-2_69).
- [W12] Richard Vuduc, Shoaib Kamil, Jen Hsu, Rajesh Nishtala, James W. Demmel, and Katherine A. Yelick. Automatic performance tuning and analysis of sparse triangular solve. In *Proc. Wkshp. Performance Optimization of High-level Languages and Libraries (POHLL), at ACM Int'l. Conf. Supercomputing (ICS)*, New York, USA, June 2002.  
**Winner, Best Presentation; Winner, Best Student Paper.**  
<http://www.ece.lsu.edu/jxr/pohll-02/papers/vuduc.pdf>.
- [W13] Richard Vuduc, James Demmel, and Jeff Bilmes. Statistical modeling of feedback data in an automatic tuning system. In *Proc. ACM Wkshp. Feedback-Directed Dynamic Optimization (FDDO), at Int'l. Symp. Microarchitecture (MICRO)*, Monterey, CA, USA, December 2000.  
**Winner, Best Presentation.**  
<http://www.eecs.harvard.edu/~smith/fddo3/papers/107.ps>.
- [W14] Richard Vuduc and James W. Demmel. Code generators for automatic tuning of numerical kernels: Experiences with FFTW. In *Proc. Semantics, Applications, and Implementation of Program Generation (SAIG), at ACM SIGPLAN Int'l. Conf. Functional Programming (ICFP)*, Montréal, Canada, September 2000.  
[http://dx.doi.org/10.1007/3-540-45350-4\\_14](http://dx.doi.org/10.1007/3-540-45350-4_14).

### 3.6 OTHER PUBLICATIONS (NON-REFEREED)

- [O1] Yuan Zhao, Qing Yi, Ken Kennedy, Dan Quinlan, and Richard Vuduc. Parameterizing loop fusion for automated empirical tuning. Technical Report UCRL-TR-217808, Center for Applied Scientific Computing, Lawrence Livermore National Laboratory, California, USA, December 2005.  
<http://dx.doi.org/10.2172/890608>.
- [O2] E. Jason Riedy and Richard Vuduc. Microbenchmarking the Tera MTA. <http://vuduc.org/pubs/riedy99-tera-report.pdf>, May 1998.

### 3.7 INVITED KEYNOTES AND TALKS

- [T1] On the limits of and opportunities for GPU acceleration. Scientific Discovery through Advanced Computing (SciDAC), US Department of Energy, Chattanooga, Tennessee, July 13 2010.  
<http://computing.ornl.gov/workshops/scidac2010>.

*New '10-'11 Item*

*New '10-'11 Item*

- [T2] Generalized  $n$ -body problems: From fast algorithms to fast code. Dagstuhl Seminar 10191 on Program Composition and Optimization: Autotuning, Scheduling, Metaprogramming and Beyond, May 2010.

<http://www.dagstuhl.de/en/program/calendar/semhp/?semnr=10191>.

*New '10-'11 Item*

- [T3] Optimizing and tuning the fast multipole method for state-of-the-art multicore architectures. SIAM Conf. Parallel Processing (SIAM PP), Seattle, WA, USA, February 2010.

<http://www.siam.org/meetings/pp10/>.

*New '10-'11 Item*

- [T4] Parallel  $n$ -body solvers: Lessons learned in the multicore/many-core era. Yahoo! Labs, Bangalore, India, January 2010.

- [T5] Parallel  $n$ -body solvers: Lessons learned in the multicore/many-core era. NIPS Wkshp. Large-scale Machine Learning: Parallelism and Massive Datasets, <http://www.select.cs.cmu.edu/meetings/biglearn09/>, Vancouver, British Columbia, Canada, December 2009.

- [T6] Toward scalable particle methods for physics and data analysis. Wkshp. Performance Analysis on Extreme-Scale Systems and Applications, at the Los Alamos Computer Science Symposium (LACSS), Santa Fe, NM, USA, October 2009.

<http://www.lanl.gov/conferences/lacss/2009/agenda/workshops.shtml>.

- [T7] Toward a programming model for data analysis and mining. Minisymposium on The Potential of Computational Algorithms in Datamining, at the SIAM Conf. Computational Sci. Eng. (SIAM CSE), Miami, FL, USA, March 2009.

[http://meetings.siam.org/sess/dsp\\_programsess.cfm?SESSIONCODE=8103](http://meetings.siam.org/sess/dsp_programsess.cfm?SESSIONCODE=8103).

- [T8] Recent progress in autotuning. Intel Corporation, Hudson, MA, USA, August 2008.

- [T9] Expressing data structure transformations for an automatically tuned sparse matrix library. Lua Workshop, George Washington University, Washington, DC, USA, July 2008.

<http://www.lua.org/wshop08.html#vuduc>.

- [T10] Tuning sparse matrix-vector multiply for multicore. Courant Institute, New York University, USA, April 2008.

- [T11] Tuning sparse matrix-vector multiply for multicore. Oak Ridge National Laboratory, Oak Ridge, TN, USA, April 2008.

- [T12] Tuning sparse matrix-vector multiply for multicore. Minisymposium on Algorithms and Optimizations Targeting Multi-Core Architectures, SIAM Conf. Parallel Processing (SIAM PP), Atlanta, GA, USA, March 2008.

[http://meetings.siam.org/sess/dsp\\_programsess.cfm?SESSIONCODE=6762](http://meetings.siam.org/sess/dsp_programsess.cfm?SESSIONCODE=6762).

- [T13] Automated data structure transformations for sparse matrix kernels. Minisymposium on Tools for the Development of High-performance Scientific Applications, SIAM Conf. Parallel Processing (SIAM PP), Atlanta, GA, USA, March 2008.  
[http://meetings.siam.org/sess/dsp\\_programsess.cfm?SESSIONCODE=6765](http://meetings.siam.org/sess/dsp_programsess.cfm?SESSIONCODE=6765).
- [T14] OSKI: A library of automatically tuned sparse matrix kernels. Int'l. Wkshp. Automatic Performance Tuning (iWAPT), Tōkyō, Japan, October 2006.  
<http://iwapt.org/2006/>  
*Invited Keynote.*
- [T15] OSKI: A library of automatically tuned sparse matrix kernels. Kyōtō University, Japan, October 2006.
- [T16] Automatically tuned sparse matrix kernels. High-Performance Computing Seminar, Pomona College, USA, September 2006.
- [T17] The Optimized Sparse Kernel Interface. Bay Area Scientific Computing Day, Livermore, CA, USA, March 2006.  
<https://computation.llnl.gov/casc/workshops/bascd/2006/>.
- [T18] Tutorial: The ROSE source-to-source infrastructure. IEEE Int'l. Conf. Parallel Architecture and Compilation Techniques (PACT), St. Louis, MO, USA, September 2005.  
<http://www.informatik.uni-trier.de/~ley/db/conf/IEEEpact/IEEEpact2005.html>.
- [T19] Automatically tuned sparse matrix kernels. University of Rome, Tor Vegata, Italy, July 2005.

### 3.8 RESEARCH PROPOSALS AND GRANTS (PRINCIPAL INVESTIGATOR)

#### 3.8.1 *Approved and Funded*

1. TOWARD EXASCALE PSEUDO-SPECTRAL CODES FOR TURBULENCE SIMULATIONS ON GPGPUs  
 PI: Richard Vuduc (CoC/CSE)  
 Co-PI: P.K. Yeung (CoE/AE)  
 Georgia Institute of Technology, Institute for Data and HPC Type I Seed Grant Program  
 AY 2010–2011
2. CAREER: AUTOTUNING FOUNDATIONS FOR EXASCALE SYSTEMS  
 PI: Richard Vuduc  
 National Science Foundation, Award 0953100  
 June 2010–May 2014
3. FOUNDATIONS FOR A HIGH-SPEED, LOW-POWER DATA ANALYTICS APPLIANCE

*New '10-'11 Item*

*New '10-'11 Item*

PI: Richard Vuduc  
 Defense Advanced Research Projects Agency  
 Computer Science Study Group, Phase 2  
 June 2010–May 2012

4. AUTOTUNING APPLICATIONS FOR EMERGING MULTICORE PLATFORMS

PI: Richard Vuduc  
 Co-PI: Chi-Keung Luk (Intel Corporation)  
 2009–2010

5. FROM ALGORITHMS TO HARDWARE: BLUEPRINTS TO SUPPORT COOPERATIVE HARDWARE/SOFTWARE PREFETCHING IN DATA-INTENSIVE APPLICATIONS

Co-PIs: Hyesoon Kim, Richard Vuduc  
 Raytheon Faculty Fellowship  
 August 2009–May 2010

6. ALGORITHMS AND SOFTWARE WITH TUNABLE PARALLELISM

PI: Richard Vuduc  
 Defense Advanced Research Projects Agency  
 Computer Science Study Group (RA-08-52)  
 April 2009–April 2010

7. THOR: A NEW PROGRAMMING MODEL FOR DATA ANALYSIS AND MINING

PI: Richard Vuduc  
 Co-PI: Alexander G. Gray (CoC/CSE)  
 National Science Foundation, Award 0833136  
 September 2008–August 2010

3.9 RESEARCH PROPOSALS AND GRANTS (CONTRIBUTOR)

3.9.1 *Approved and Funded*

1. MLPACK: SCALABLE MACHINE LEARNING SOFTWARE FOR LARGE-SCALE SCIENCE

PI: Alexander Gray  
 Co-PIs: Richard Vuduc, Haesun Park, Yu (Cathy) Jiao, John McDonald, Gordon Richards, Alexander Shapiro, Jeffrey Vetter  
 Georgia Institute of Technology, Institute for Data and HPC Type II Seed Grant Program  
 AY 2010–2011

2. VANCOUVER: DESIGNING A NEXT-GENERATION SOFTWARE INFRASTRUCTURE FOR HETEROGENEOUS EXASCALE COMPUTING

PI: Jeffrey Vetter (CoC/CSE + ORNL)

Co-PIs: Wen-Mei Hwu (University of Illinois, Urbana-Champaign),  
 Allen Maloney (University of Oregon), Richard Vuduc (CoC/CSE)  
 Department of Energy, X-Stack  
 September 2010–August 2013

*New '10-'11 Item*

3. COLLABORATIVE RESEARCH: ESTABLISHING A CENTER FOR HYBRID MULTICORE PRODUCTIVITY RESEARCH  
 PI: David A. Bader (CoC/CSE), Thomas Conte (CoC/CS)  
 Co-PIs: Nathan Clark (CoC/CS), Ada Gavrilovska (CoC/CS), Hyesoon Kim (CoC/CS), Richard Vuduc (CoC/CSE)  
 National Science Foundation, [Award 0934114](#)  
 August 2009–July 2010
  
4. QAMELEON: HARDWARE/SOFTWARE CO-OPERATIVE AUTOMATED TUNING FOR HETEROGENEOUS ARCHITECTURES  
 PI: Hyesoon Kim (CoC/CS)  
 Co-PI: Richard Vuduc (CoC/CSE)  
 National Science Foundation, joint with Semiconductor Research Corporation, [Award 0903447](#)  
 August 2009–July 2012
  
5. COLLABORATIVE RESEARCH: ESTABLISHING AN I/UCRC CENTER FOR MULTICORE PRODUCTIVITY RESEARCH (CMPR)  
 PI: Milt Halem (University of Maryland, Baltimore County)  
 Co-PIs: David A. Bader (CoC/CSE), Paul Woodward (University of Minnesota), Paul Sheldon (University of California, San Diego)  
 Contributors: Richard Vuduc (CoC/CSE)  
 National Science Foundation, [Award 0831110](#)  
 August 2008

### 3.10 RESEARCH HONORS AND AWARDS

1. November 2010: Finalist, Gordon Bell Prize at ACM/IEEE Conf. Supercomputing (SC) *New '10-'11 Item*
2. April 2010: College of Computing's Outstanding Junior Faculty Research Award *New '10-'11 Item*
3. April 2010: Winner, Best paper (software track) at the IEEE Int'l. Parallel and Distributed Processing Symp. (IPDPS). *New '10-'11 Item*
4. January 2010: NSF CAREER Awardee. *New '10-'11 Item*
5. November 2009: Finalist, Best paper at SC
6. November 2009: Winner, R&D 100 Award for ROSE (joint with Lawrence Livermore National Laboratory)
7. April 2009: Panelist, DARPA Computer Science Study Panel (CSSP). One of twelve panelists selected nationally among untenured US citizen professors.

8. July 2006: Winner, Best paper at Parallel and Distributed Testing and Debugging (PADTAD)
9. September 2004: Winner, Best paper at Int'l. Conf. Parallel Processing (ICPP)
10. June 2002: Winner, Best student presentation at Workshop on Performance Optimization of High-level Languages and Libraries, joint with the ACM Int'l. Conf. Supercomputing (ICS)
11. June 2002: Winner, Best student paper at Workshop on Performance Optimization of High-level Languages and Libraries, joint with ICS
12. November 2002: Finalist, Best student paper at SC
13. December 2000: Winner, Best presentation at Workshop on Feedback-directed Dynamic Optimization (FDDO), joint with the IEEE Int'l. Symp. Microarchitecture (MICRO)

# 4

## SERVICE

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### 4.1 PROFESSIONAL ACTIVITIES

#### 4.1.1 *Membership and Activities in Professional Societies*

1. 2010–2012, Secretary (elected), Society for Industrial and Applied Mathematics, Activity Group on Supercomputing (SIAM/CSE) *New '10-'11 Item*
2. Member, Association for Computing Machinery (ACM)
3. Member, SIAM
4. Member, Institute of Electrical and Electronics Engineers (IEEE)

#### 4.1.2 *Conference Committee Activities*

##### 4.1.2.1 *Conferences*

1. Program Committee, IEEE Int'l. Parallel and Distributed Processing Symp. (IPDPS), 2011 *New '10-'11 Item*
2. Program Committee, IEEE/ACM Int'l. Symp. Code Generation and Optimization (CGO), 2011 *New '10-'11 Item*
3. Program Committee, Int'l. Conf. High-Performance Computing (HiPC), 2010 *New '10-'11 Item*
4. Program Committee, ACM/IEEE Conf. Supercomputing (SC), 2010 *New '10-'11 Item*
5. Program Committee, Conf. Partitioned Global Address Space (PGAS) Programming Models, 2010 *New '10-'11 Item*
6. Program Committee, Int'l. Mtg. High-Performance Computing for Computational Sci. (VECPAR), 2010 *New '10-'11 Item*
7. Program Committee, ACM Int'l. Conf. Computing Frontiers (CF), 2010 *New '10-'11 Item*
8. Program Committee, ACM SIGPLAN Symp. Principles and Practice of Parallel Programming (PPoPP), 2010 *New '10-'11 Item*
9. Program Committee, SC, 2009
10. Program Committee, Int'l. Conf. High-Performance Computing (HiPC), 2008
11. Program Committee, IEEE Int'l. Conf. Computational Sci. Eng. (CSE), 2008
12. Poster Committee, SC, 2005

4.1.2.2 *Workshops**New '10-'11 Item*

1. Program Committee, *International Workshop on Multicore Software Engineering* at ACM/IEEE Int'l. Conf. Software Eng., 2011

*New '10-'11 Item*

2. Program Committee, *Workshop on Application/Architecture Co-design for Extreme-scale Computing* at IEEE Cluster, 2010

*New '10-'11 Item*

3. Organizing Committee, *Concurrent Collections Workshop* at LCPC, 2010

*New '10-'11 Item*

4. Program Committee, *Int'l. Wkshp. Automatic Performance Tuning (iWAPT)*, 2010

*New '10-'11 Item*

5. Local Organizing Chair, *IEEE Int'l. Symp. Workload Characterization (IISWC)*, 2010

*New '10-'11 Item*

6. Steering Committee, *Wkshp. Statistical Machine Learning applied to ARchitecture and compilaTion (SMART)*, 2010

*New '10-'11 Item*

7. Program Committee, *USENIX Wkshp. Hot Topics in Parallelism (HotPar)*, 2010

8. Program Committee, *iWAPT*, 2009

9. Program Committee, *SMART*, 2008

10. Program Committee, *iWAPT*, 2008

11. Program Committee, *IEEE Int'l. Wkshp. Parallel and Distributed Scientific and Eng. Computing (PDSEC)*, 2008

12. Program Committee, *iWAPT*, 2007

13. Program Committee, *Wkshp. Performance Optimization of High-level Languages and Libraries (POHLL)*, 2007

14. Program Committee, *SMART*, 2007

15. Program Committee, *POHLL*, 2006

4.1.2.3 *Workshop and External Course Organization**New '10-'11 Item*

1. Co-organizer, *Tutorial on Performance analysis and tuning for GPGPUs*, at the *ACM/IEEE Int'l. Symp. Microarchitecture (MICRO)*, 2010

*New '10-'11 Item*

2. Co-organizer, *Wkshp. Libraries and Autotuning for Petascale Applications*, for the US Dept. of Energy Center for Scalable Application Development Software (DOE CScADS), 2010

*New '10-'11 Item*

3. Co-organizer, *Tutorial on Performance Analysis and Optimization for GPUs*, at NSF TeraGrid Conf., 2010

4. Co-organizer, *Wkshp. Libraries and Autotuning for Petascale Applications*, for US DOE CScADS, 2009

5. Co-organizer, *Wkshp. Automatic Tuning for Petascale Systems*, for US DOE CScADS, 2008
6. Co-organizer, Minisymposium on Adaptive Tools and Frameworks for High-Performance Numerical Computations, SIAM Parallel Processing, 2006
7. Co-organizer, Tutorial on “ROSE C/C++ Source-to-source Translator,” Int’l. Conf. Parallel Architectures and Compilation Techniques (PACT), 2005

#### 4.2 ON-CAMPUS GEORGIA TECH COMMITTEES

1. Computational Science and Engineering (CSE) Graduate Admissions and Recruiting Committee, CoC, 2010–2011 Academic Year (AY). *New '10-'11 Item*
2. Undergraduate Curriculum Advisory Committee on Parallel Computing, College of Computing (CoC), 2009–2010 AY. *New '10-'11 Item*
3. CSE, Qualifying Exam Committee, CoC, 2009–2010 AY.
4. CSE Chair Advisory Committee, CoC, 2009–2010 AY.
5. Computer Science (CS) PhD Recruiting Weekend Committee, CoC, 2008–2009 AY.
6. CSE Review of the Chair Committee, CoC, 2008–2009 AY.
7. CSE Faculty Recruiting Committee (FRC), CoC, 2008–2009 AY.
8. Faculty Computer Committee (FCC), CoC, 2008–2009 AY.
9. CSE PhD Admissions Committee, CoC, 2008–2009 AY.
10. CS PhD Admissions Committee, CoC, 2008–2009 AY.
11. CSE Qualifying Exam Committee, CoC, 2008–2009 AY.
12. CSE Chair Advisory Committee, CoC, 2008–2009 AY.
13. CSE FRC, CoC, 2008–2009 AY.
14. CSE PhD Admissions Committee, 2007–2008 AY.
15. CS PhD Admissions Committee, 2007–2008 AY.
16. Teaching Assistanceship Requirement Grandfathering Committee, CoC. 2007–2008 AY.
17. FCC, CoC, 2007–2008 AY.
18. Awards Committee, CoC, 2007–2008 AY.

## 4.3 MEMBER OF PH.D. EXAMINING COMMITTEES

1. Nitin Arora, College of Engineering (CoE), Aerospace Engineering (AE)  
Thesis Proposal, Fall 2010  
Advisor: Ryan RUSSELL (CoE/AE)  
*New '10-'11 Item*
2. Jiang Bian, College of Computing (CoC), Computational Science and Engineering (CSE)  
Thesis Proposal, Spring 2010  
Advisor: Hongyuan ZHA (CoC/CSE)  
*New '10-'11 Item*
3. Dongryeol Lee, College of Computing (CoC), Computational Science and Engineering (CSE)  
Thesis Proposal, Spring 2010  
Advisor: Alexander G. GRAY (CoC/CSE)  
*New '10-'11 Item*
4. Sooraj Bhat, College of Computing (CoC), Computer Science (CS)  
Thesis Proposal, Spring 2010  
Advisor: Alexander G. GRAY (CoC/CSE)  
*New '10-'11 Item*
5. William March, CoC, CSE  
Qualifying Exam, Spring 2010  
Advisor: Alexander G. GRAY (CoC/CSE)  
*New '10-'11 Item*
6. Aparna Chandramowlishwaran, CoC, CSE  
Qualifying Exam, Fall 2009  
Advisor: Richard VUDUC (CoC/CSE)
7. Mingxuan Sun, CoC, CS  
Qualifying Exam, Fall 2009  
Advisor: Guy LEBANON
8. Parikshit Ram, CoC, CS  
Qualifying Exam, Fall 2009  
Advisor: Alexander GRAY
9. Asif Salahuddin, CoE, Mechanical Engineering (ME)  
Thesis Proposal, Summer 2009  
Advisor: Cyrus AIDUN (CoC/ME)
10. Murat Efe Guney, CoE, Civil Engineering (CE)  
Thesis Proposal, Summer 2009  
Advisor: Kenneth WILLS (CoE/CE)
11. Virat Agarwal, CoC, CSE  
Thesis Proposal, Summer 2009  
Advisor: David BADER
12. Rahul Sampath, CoC, CSE  
Spring 2009  
Ph.D. Thesis: A parallel geometric multigrid method for finite elements on octrees applied to elastic image registration  
Advisor: George BIROS (CoC/CSE + CoE/BME)

13. Steven P. Crain, CoC, CSE  
Qualifying Exam, Spring 2009  
Advisor: Hongyuan ZHA (CoC/CSE)
  14. Haw-Jing "Michael" Lo, CoE, Electrical and Computer Engineering (ECE)  
Spring 2009  
Ph.D. Thesis: Design of a reusable distributed arithmetic filter and its application to the affine projection algorithm  
Advisor: David ANDERSON (CoE/ECE)
  15. Jaegul Choo, CoC, CSE  
Qualifying Exam, Spring 2009  
Advisor: Haesun PARK (CoC/CSE)
  16. Rahul Sampath, CoC, CSE  
Thesis Proposal, Spring 2009  
Advisor: George BIROS (CoC/CSE + CoE/BME)
  17. Manisha Gajbe, CoC, CS  
Qualifying Exam, Spring 2009  
Advisor: David BADER (CoC/CSE)
  18. Hasan Abbasi, CoC, CS  
Thesis Proposal, Spring 2009  
Advisor: Karsten SCHWAN
  19. Dongreol Lee, CoC, CS  
Qualifying Exam, Fall 2008  
Advisor: Alexander GRAY (CoC/CSE)
  20. Ryan Riegel, CoC, CS  
Qualifying Exam, Spring 2008  
Advisor: Alexander GRAY (CoC/CSE)
  21. Alfred Park, CoC, CS  
Ph.D. Thesis: Master/worker parallel discrete event simulation  
Advisor: Richard FUJIMOTO (CoC/CSE)
  22. Kamesh Madduri, CoC, CS  
Ph.D. Thesis: A high-performance framework for analyzing massive complex networks  
Advisor: David BADER (CoC/CSE)
  23. Michael P. Holmes, CoC, CS  
Ph.D. Thesis: Multi-tree Monte Carlo methods for fast, scalable machine learning  
Advisors: Charles ISBELL (primary), Alexander GRAY
- 4.4 MEMBER OF MASTERS EXAMINING COMMITTEES
1. Sundaresan Venkatasubramanian, CoC, CS  
Spring 2009  
Thesis: Tuned and wildly asynchronous algorithms for hybrid CPU/GPU platforms  
Advisor: Richard VUDUC

2. Rick Quax, CoC, CS  
Fall 2008  
Thesis: Modeling and simulating the propagation of infectious diseases using complex networks  
Advisor: David BADER
3. Amrita Mathuriya, CoC, CS  
Fall 2008  
Thesis: GTfold: A scalable multicore code for RNA secondary prediction  
Advisor: David BADER

4.5 RESEARCH PROJECT REVIEWER

- New '10-'11 Item*
1. Department of Energy, 2010
  2. Department of Energy, 2009
  3. National Science Foundation, 2008

4.6 CIVIC AND OTHER ACTIVITIES

- New '10-'11 Item*
1. Judge, Georgia Science and Engineering Fair, 2010
  2. Judge, Georgia Science and Engineering Fair, 2009
  3. Speaker, Advice on Preparing a Research Statement, CoC Job Hunting Seminar, 2009
  4. Reader, Georgia Junior Humanities and Science Fair, 2008
  5. Speaker, Advice on Preparing a Research Statement, CoC Job Hunting Seminar, 2008

## PROFESSIONAL RECOGNITION

## 5.1 INVITED CONFERENCE SESSION CHAIRMANSHIPS

1. Session Chair, Panel on Unconventional Wisdom in Multicore (at IPDPS), Atlanta, GA, April 2010 *New '10-'11 Item*
2. Session Co-Chair, Workshop on Parallel Matrix Algorithms and Applications (PMAA), Basel, Switzerland, June 2010 *New '10-'11 Item*
3. Session Chair, Symposium on Computing Systems and Technologies, Georgia Institute of Technology, Atlanta, GA, April 2008
4. Session Co-Chair, Mini-symposium on Adaptive Tools and Frameworks for High Performance Numerical Computations, SIAM Parallel Processing, 2006

## 5.2 INVITED PANEL SPEAKER

1. Panel on MPI+Manycore Issues and Plans, Sandia National Laboratories Computer Science Research Institute (CSRI) Workshop on Next-generation Scalable Application: When MPI-only is Not Enough, May 2008.
2. Panel on New HPC-Challenge Benchmark Candidates, Georgia Tech, November 2008.

## 5.3 REVIEWER WORK FOR TECHNICAL PUBLICATIONS

5.3.1 *Book proposals*

1. CRC Press, 2008.

5.3.2 *Books*

1. CRC Press, 2010. *New '10-'11 Item*

5.3.3 *Journals*

1. Int'l. J. High Performance Computing Applications (IJHPCA), 2002, 2004, 2009.
2. IEEE Trans. Par. Distrib. Sys. (TPDS), 2009.
3. ACM Trans. Architecture and Code Optimization (ACM TACO), 2009.
4. J. Parallel Computing (ParCo), 2008, 2009.

5. SIAM J. Scientific Computing (SISC), 2008, 2009.
6. ACM Trans. Mathematical Software (ACM-TOMS), 2008.
7. J. Parallel and Distributed Computing (JPDC), 2008.
8. Software: Practice and Experience, 2008.
9. Concurrency and Computation: Practice and Experience (CC:PE), 2007.
10. Parallel Processing Letters, 2004.
11. Proc. IEEE, 2004.
12. J. Functional Programming (JFP), 2001.

#### 5.3.4 *Conferences and Workshops*

*New '10-'11 Item*

1. IEEE Int'l. Par. Distrib. Processing Symp. (IPDPS), 2010.

*New '10-'11 Item*

2. Workshop on General-Purpose Computation on Graphics Processing Units (GPGPU), 2009, 2010.
3. Winter Simulation Conference (WSC), 2009.
4. Int'l. Symp. Computer Architecture (ISCA), 2008.
5. Computing Frontiers (CF), 2008.
6. Int'l Conf. High-Performance Computing and Communications (HPCC), 2007.
7. ACM/IEEE Conf. Supercomputing (SC), 2007.
8. ACM Int'l. Conf. Supercomputing (ICS), 2007.
9. Network and Parallel Computing (NPC), 2006.
10. Euro-Par, 2006.
11. Combinatorial Scientific Computing, 2005.
12. ACM Symposium on Parallel Algorithms and Architectures (SPAA), 2003, 2004.
13. ACM Conf. Programming Language Design and Implementation (PLDI), 2003.
14. Int'l. Conf. Computational Science (ICCS), 2001.
15. ACM Conf. Principles of Programming Languages (POPL), 2000.